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PATENT  
Attorney Docket No. SEL 156 DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
Shunpei Yamazaki, et al.

Serial No.: 09/874,670

Filed: June 5, 2001

Examiner: Lalrinfamkin Hmar Malsawma

Art Unit: 2825

For: SEMICONDUCTOR DEVICE AND  
MANUFACTURING METHOD THEREOF

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Shannon Wallace

Name of applicant, assignee, or Registered Rep.  
Shannon Wallace 8/19/04  
Signature Date

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT D

Sir/Madam:

In response to the Office Action mailed March 3, 2004, a three month extension of time  
being submitted herewith, please amend the above-identified patent application as follows.

**IN THE DRAWINGS:**

The attached sheets of drawings include changes to Figs. 1 and 3C to complete the line of the upper surface of the insulating film 131. This error occurred in the preparation of the formal drawings.

Attachment: Replacement Sheets

Annotated Sheets Showing Changes